



Department of Electronics and Communication Engineering
K.S.R.M COLLEGE OF ENGINEERING
AUTONOMOUS
FIRST COURSE REVIEW COMMITTEE REPORT(CRC) 2021-22

Department	ECE	Year/Semester	I M.Tech/I
Course Name	RTL Simulation and Synthesis with PLDs Lab	Course Code	1854112

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Sri Y. Venkateswara Raju	Coordinator	Asst. Professor	
2.	Sri P. Krishna Teja Yadav	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To design various combinational and sequential circuits Using Verilog HDL. 2. To design FSM machines, Vending machines. 3. Implement UART/USART in Verilog.	1. Students are facing problems in designing Sequential circuits 2. Some of the students are absent for the labs	1.The availability of the licenced Xilinx Vivado software . 2. The easy way of Implementing the experiments in the hardware.	1.Difficult to identify the components and their connection from the students point of view. Difficulty to understand the usage of the experiments in application point of view.	To overcome this problem a demo will be conducted for the components and their connections. Explaining the applications of each experiment.
Sri P. Krishna Teja Yadav	Sri Y. Venkateswara Raju	Dr.G.Hemalatha		
Member	Coordinator	HOD Professor & H.O.D. Department of E.C.E.		

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FIRST COURSE REVIEW COMMITTEE REPORT(CRC) 2021-22

Department	ECE	Year/Semester	I M.Tech/I
Course Name	RTL Simulation and Synthesis with PLDs	Course Code	1854101

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Sri Y.Venkateswara Raju	Coordinator	Asst. Professor	
2.	Smt. S. Sharmila Banu	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. Develop the Verilog HDL to design a digital circuit. 2. Verify the functionality of the digital designs using PLDs. 3. Understand the Static Timing Analysis and clock issues in digital circuits .	1. Students are faced difficulty to understand the Verilog HDL. 2. Some of the students are irregular.	1. The basics of the subject are studied in UG Level.	1. Difficult to understand the concepts from the students point of view. Difficulty to understand the concept Timing Analysis and clock issues in digital circuits.	The Learning sessions will be conducted to avoid the difficulty.

 Smt. S. Sharmila Banu	 Sri Y.Venkateswara Raju	 Dr.G.Hemalatha
Member	Coordinator	HOD

Professor & H.O.D,
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Department	ECE	Year/Semester	M.Tech I/I
Course Name	CPLD, FPGA architectures and applications.	Course Code	1854111

Committee Members:

S.No	Name	Role	Designation	Signature
1.	Smt.K.Divyalakshmi	Coordinator	Asst.Prof	
2.	Sri.P.Krishna teja yadav	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1.To check the Uniform Coverage of syllabus according to the Internal Examinations. 2.To overcome the shortcomings from students' point of view, if any	1.Students want to listen to teaching in E-class mode because they understand the significance and working of FPGA,CPLD architectures.. 2. The concerned faculty identified a few of them as not attending regularly.	1.Usage of Pen tablet and YOUTUBE videos for explaining each and every topic . 2. The related text books and solved Designing problems are sent before commencement of topics to be covered.	1.Difficult to understand the FPGA,CPLD Architectures. 2. Difficult to Understand the Power and clock distribution terminology.	1. To create projects on VLSI design. 2. Explaining more problems will be expertizing the FPGA,CPLD implementation

Member	K. Divyalakshmi Coordinator	HOD

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Department	ECE	Year/Semester	I M.Tech/I
Course Name	Design for testability	Course Code	1854107

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Smt. S. Sharmila Banu	Coordinator	Asst. Professor	
2.	Sri Y.Venkateswara Raju	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To analyze the digital circuits with the presence of faults. 2.To generate the test patterns. 3.To understand the concept of controllability and observability. 4.To determine the built in self test.	1. Students are new to the course and for finding the faults in the digital circuits. 2. Some of the students are irregular.	1. The basics of the subject are studied in UG Level. 2.To know the different techniques of generating the test patterns.	1.Difficult to understand fault finding - from the students point of view. Difficulty to understand the concept of controllability and observability	The Learning sessions will be conducted to avoid the difficulty.

 Sri Y. Venkateswara Raju Member	 Smt. S. Sharmila Banu Coordinator	 Dr.G.Hemalatha HOD Professor & H.O.D.
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FIRST COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22

Department	ECE	Year/Semester	M. Tech I Sem
Course Name	Research Methodology & IPR	Course Code	1854103

S.No	Name	Role	Designation	Signature
1.	Sri A. Valli Bhasha	Coordinator	Assistant Professor	A. V. Bhasha
2.	Sri P. Krishna Teja Yadav	Member	Assistant Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To give an overview of the research methodology and explain the technique of defining a research problem 2. The main objective of intellectual property law is to encourage innovation and to provide incentives.	1. Students facing difficulty in understanding various research designs and their characteristics 2. The concerned faculty identified few students are not attending regularly.	1. Students must know literature search, its review, developing theoretical and conceptual frameworks and writing a review. 2. The related notes and study material is distributed to students for revising.	1. Student must have basic concepts for understanding this subject 2. Difficult in understanding the concepts	1. To take extra classes to revise the concepts 2. To utilize e-content for explaining more number of problems.

	A. V. Bhasha	
Member	Coordinator	ProfessHOD H.O.D.

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Department of Electronics and Communication Engineering
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Department	ECE	Year/Semester	II/III
CourseName	ANALOG CIRCUITS	CourseCode	2004303




Date: 09/01/2022

Committee Members:

S.No	Name	Role	Designation	Signature
1	GSuneelKumar	Coordinator	Asst.Prof	
2	P Swetha	Member	Asst.Prof	
3	S Sharmila Banu	Member	Asst. Prof.	

Purpose /Objective	ProblemsIdentified	ResultAnalysis		Suggestions forImprovement
		Strength	Weakness	
1.To give the	1.Students	1. The knowledge of	1.Lack of	1. To

<p>concepts related to Amplifiers.</p> <p>2. To introduce Frequency response of transistors.</p> <p>3. To overcome the short coming of the students</p>	<p>want to know the various Multistage amplifiers.</p> <p>2. Some of the students are lacking the details of transistor configurations.</p>	<p>basic operation of electronic devices.</p> <p>2. The easy way of explaining the concepts of oscillators.</p>	<p>awareness the fundamentals of multivibrators.</p> <p>2. Difficulty to understand the concepts of tuned amplifiers.</p>	<p>overcome this problem a demo will be conducted for all the students covering the basics of multivibrators</p> <p>2. Explaining the basics of tuned amplifiers.</p>
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 Member	 Coordinator	 HOD
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Department	ECE	Year/Semester	II/III
Course Name	Simulation Lab	Course Code	2004305

Committee Members:

S.No	Name	Role	Designation	Signature
1.	P.Lokeshwara Reddy	Coordinator	Assistant Professor	
2.	P.Subbarayudu	Member 1	Assistant Professor	
3.	S.Munawar Ali	Member 2	Assistant Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To impart the Programming skills. 2. To assess the significance of Lab. 3. To understand significance of Basic Signals. 4. To identify and sort out any problems in executing the program.	1. Students are facing difficulty in understanding Programming concepts. 2. Some of the students are irregular to the lab. 3. Few Students were unable to execute the code.	1. Explaining the programs using white board and marker. 2. Utilizing e-content & Power point presentations for explaining the topics.	1. Student does not have good knowledge on pre requisites for understanding this Lab. 2. Difficult in covering the syllabus uniformly.	1. Take extra classes to revise the Pre-requisites concepts. 2. Take extra hours to practice. 3. Use white board and marker pen to explain the programs.

Member 1

Member 2

Coordinator

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Department	ECE	Year/Semester	II/III
Course Name	Analog Circuits Lab	Course Code	1804408

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	M. Prabhakar	Coordinator	Asst.Prof	
2.	Dr.P.Giri Prasad	Member	Asst.Prof	
3.	Miss.P. Swetha	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To conduct awareness for the students on the electronic circuits experiments. 2.To analyze and practice various Electronic circuits 3. To overcome the short comings of the students	1. Students want the demonstration the experiments which are going to be dealt in the lab. 2. Some of the students are absent for the labs	1.The availability of different amplifiers hardware kits and software tools 2.The easy way of explaining the experiments to the students both hardware and simulation.	1. Lack of awareness in the fundamentals of electronic circuits 2. Difficulty to understand the usage of the experiments in application point of view.	1. To overcome this problem a demo will be conducted for all the experiments before the conduction of the lab. 2. Explaining the applications of each experiment and allow the students to mention in the records.

 Member	 Member	 Coordinator	 HOD
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Department	ECE	Year/Semester	III B.Tech/V
Course Name	Digital IC Applications	Course Code	1804505

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Sri Y. Venkateswara Raju	Coordinator	Asst. Professor	
2.	Miss.P. Swetha	Member	Asst. Professor	
3.	Kavitha	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
<p>1. Understand CMOS, Bipolar logic families and fundamentals of Verilog HDL Programming.</p> <p>2. Apply the concepts of Verilog HDL for modeling and simulation of digital logic circuits</p>	<p>1. Students are new to the CMOS design based circuits.</p> <p>2. Some of the students are weak in digital circuits and Hardware Description Language.</p>	<p>1. The students learnt the basics of CMOS Circuits design.</p> <p>2. the concepts of designing CMOS circuits based on digital systems are explained using PPT and verilog concepts are practically shown and the simulation process for easy understanding</p>	<p>1. Difficult to analyze the design of circuits using CMOS-from the students point of view..</p> <p>Difficulty to understand the Verilog Concepts</p>	<p>The practicing sessions are conducted and explained the basics of the designing and the Verilog Simulation.</p>

 Miss.P. Swetha	 Sri Y. Venkateswara Raju	 Dr.G.Hemalatha
Member	Coordinator	HOD

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Department	ECE	Year/Semester	III/V
Course Name	Antennas & Wave Propagation	Course Code	1804506

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	K.Pavan Kumar	Coordinator	Asst.Prof	
2.	S.Jabeen	Member	Asst.Prof	
3.	S.sudheer Kumar	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To check the uniform coverage of syllabus as per the lesson plan. 2. To get the feedback of students about course.	1. Students may feel difficulty in derivation part of the antenna characteristics. 2. Difficulty in analyzing the radiation Characteristics of different antennas.	1. Fundamentals on Electromagnetics. 2. More attention is given on weak students. 3. Usage of LMS tools.	1. Difficult to understand derivations. 2. Difficult to solve critical problems.	1. Introduction classes are required on fundamentals of Electromagnetics. 2. More number of problems are need to be solved.

Member	Member	Coordinator	HOD H.O.D.

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FIRST COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22

Department	ECE	Year/Semester	III/ V
Course Name	Microprocessors & Microcontrollers	Course Code	1804501

Committee Members:

S.No.	Name	Role	Designation	Signature
1	Sri R.V. Sreehari	Coordinator	Assoc. Professor	
2	Dr.S.L. Pratapa Reddy	Member 1	Assoc. Professor	
3	Kavitha	Member 2	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To assess the significance of this subject at this level.	1. Students are facing difficulty in understanding Programming concepts.	1. Usage of Pen tablet forexpaining Topics in Online mode.	1. Student must have good knowledge on pre-requisites for understanding this subject.	1. Take extra classes to revise the Pre-requisites concepts.
2. To maintain the uniform Coverage of syllabus according to the Internal/External Examinations schedule.	2. The concerned faculty identified few topics which takes much time to deliver.	2. Utilizing e-content & Power point presentations for explaining the lengthier topics.	2. Difficult in covering the syllabus uniformly.	2. Use Power point presentations for covering the syllabus uniformly. Give the related notes and videos lectures to the students for revising the topics.
3. To identify and sort out any problems in understanding the concepts.	3. Few Students requested for more practical applications.	3. The Subject is having practical significance in modern applications.	3. Few students are not familiar with modern applications.	3. Take extra hours to discuss the practical applications.

Member 1

Member 2

Coordinator

HOD

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Department	ECE	Year/Semester	III/V
Course Name	Computer Organization	Course Code	1804503

Committee Members:

S.No	Name	Role	Designation	Signature
1.	P.Lokeshwara Reddy	Coordinator	Assistant Professor	
2.	A. Sanjeeva Reddy	Member 1	Assistant Professor	
3.	Kavitha	Member 2	Assistant Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To know about various functional units of computer. 2. To understand micro programmed control unit.	1. The concerned faculty identified few topics which takes much time to deliver. 2. Some of the students are irregular to the classes.	1. Usage of Pen tablet for explaining each and every topic in online mode. 2. Utilizing e-content & Power point presentations for explaining the lengthier topics. 3. Conducting of assignments and quizzes frequently.	1. Student does not have good knowledge on pre-requisites for understanding this subject. 2. Difficult in covering the syllabus uniformly.	1. Take extra classes to revise the Pre-requisites concepts. 2. Give the related notes and videos lectures to the students for revising the topics.

Member 1

Member 2

Coordinator

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FIRST COURSE REVIEW COMMITTEE REPORT(CRC) 2020-21

Department	ECE	Year/Semester	III/V
Course Name	Analog and Digital IC Lab	Course Code	1804508

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Smt S.Sharmila Banu	Coordinator	Asst. Professor	
2.	Miss S.Jabeen	Member	Asst. Professor	
3.	Sri M.Prabhakar	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1.To create awareness for the students on the experiments of analog and digital IC applications.. 2.To finish the experiments with in time 3. To overcome the short comings of the students	1. Students are facing to give the connections on bread board and also to measure the readings in the CRO. 2. Some of the students are absent for the labs	1.The availability of the xilinx software . 2.The easy way of explaining the experiments to the students through the hardware.	1.Difficult to identify the components and their connection from the students point of view. Difficulty to understand the usage of the experiments in application point of view.	To overcome this problem a demo will be conducted for the components and their connections. Explaining the applications of each experiment and allow the students to mention in the records.

 S.Jabeen Member	 S.Sharmila Banu Coordinator	 Dr.G.Hemalatha HOD
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FIRST COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22

Department	ECE	Year/Semester	VSem
Course Name	DIGITAL SIGNAL PROCESSING	Course Code	1804502

Committee Members::

S.No	Name	Role	Designation	Signature
1	Sri P .SUBBARAYUDU	Coordinator	Asst. Professor	
2	Dr. M.V. NARAYANA	Member	Professor	M.V.N
3	Smt. HIMAJA REDDY	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To understand significance of Signals and Systems	1. Students facing difficulty in understanding the various signals and Problems.	1. More examples are explained to understand the topics	1. Student must know the fundamentals in mathematics	1. To solve additional examples and revise the concepts
2. To check the Coverage of syllabus according to the Internal Examinations.	2. The concerned faculty identified few students are not attending regularly.	2. The related notes and study material is distributed to students for revising.	2. Difficult in understanding the concepts	2. To utilize e-content for explaining more number of problems.

 Member	 Member	 Coordinator	 HOD
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Department	ECE	Year/Semester	III/V
Course Name	Linear and Digital IC Applications Lab	Course Code	2004405

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Smt S.Sharmila Banu	Coordinator	Asst. Professor	
2.	Miss P. Swetha	Member	Asst. Professor	
3.	Sri P. SubbaRayudu	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1.To create awareness for the students on the experiments of analog and digital IC applications.. 2.To finish the experiments with in time 3. To overcome the short comings of the students	1. Students are facing to give the connections on bread board and also to measure the readings in the CRO. 2. Some of the students are absent for the labs	1.The availability of the xilinx software . 2.The easy way of explaining the experiments to the students through the hardware.	1.Difficult to identify the components and their connection from the students point of view. Difficulty to understand the usage of the experiments in application point of view.	To overcome this problem a demo will be conducted for the components and their connections. Explaining the applications of each experiment and allow the students to mention in the records.

Miss P. Swetha Member	S.Sharmila Banu Coordinator	 Dr.G.Hemalatha HOD
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FIRST COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22

Department	ECE	Year/Semester	III/ V
Course Name	Microprocessors & Microcontrollers Lab	Course Code	1804507

Committee Members:

S.No.	Name	Role	Designation	Signature
1	Sri G A Sanjeeva Reddy	Coordinator	Asst. Professor	
2	Sri S Munavar Ali	Member 1	Asst. Professor	
3	Sri Y Venkateswara Raju	Member 2	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To assess the significance of this Lab at this level.	1. Students are facing difficulty in understanding Programming concepts.	1. Usage of Pen tablet forexpaining Topics in Online mode.	1. Student must have good knowledge on pre-requisites for understanding this Lab.	1. Take extra classes to revise the Pre-requisites concepts.
2. To impart the programming skills.	2. The concerned faculty identified that students are unable to understand the programs by simply copying the programs from the Lab manual.	2. Providing individual kits to the students.	2. Difficult in understanding the programs.	2. Use white board and marker pen to explain the programs.
3. To identify and sort out any problems in executing the program.	3. Few Students were unable to execute the code.	3. Providing individual kits to the students.	3. Few students are not familiar with execution procedure.	3. Take extra hours to practice.

Member 1

Member 2

Coordinator

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**Department of Electronics and Communication Engineering
FIRST COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22**

Department	ECE	Year/Semester	III/ V
Course Name	Analog communications	Course Code	1804504

Committee Members:

Date: 29/10/2021

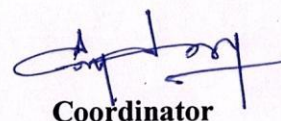
S.No.	Name	Role	Designation	Signature
1	Dr. P. Giri Prasad	Coordinator	Assit. Professor	
2	Dr. D. Arun Kumar	Member 1	Assoc. Professor	
3	Himaja Reddy	Member 2	Asst. Professor	

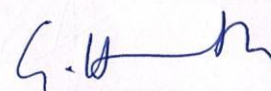
Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To assess the significance of this subject at this level.	1. Students are facing difficulty in understanding mathematical analysis of modulation and	1. Usage of ICT mode for explaining the Topics in detail with practical examples.	1. Student must have good knowledge on pre-requisites for understanding this subject.	1. Take extra classes to revise the Pre-requisites concepts.

	demodulation.			
2. To maintain the uniform Coverage of syllabus according to the Internal/External Examinations schedule.	2. The concerned faculty identified few topics which takes much time to deliver.	2. Utilizing e- content & Power point presentations for explaining the lengthier topics.	2. Difficult in covering the syllabus uniformly.	2. Use Power point presentations for covering the syllabus uniformly. Give the related notes and videos lectures to the students for revising the topics.
3. To identify and sort out any problems in understanding the concepts.	3. Few Students requested for more practical applications.	3. The Subject is having practical significance in modern applications.	3. Few students are not familiar with modern applications.	3. Take extra hours to discuss the practical applications.


Member 1


Member 2


Coordinator


HOD
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Department	ECE	Year/Semester	VII Sem
Course Name	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	Course Code	1804702

S.No	Name	Role	Designation	Signature
1.	Sri A. Valli Bhasha	Coordinator	Assistant Professor	A.V. Bhasha
2.	Sri M. Prabhakar	Member	Assistant Professor	M. Prabhakar
3.	Sri S. Sudheer Kumar	Member	Assistant Professor	SSK

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1.To Understand the principle of analog, digital voltmeters and wave analyzers	1. Students facing difficulty in understanding characteristics of instruments	1.Usage of pictures explaining Topics	1. Student must have basics in Electronic Devices and Linear integrated circuit analysis subject for understanding this subject	1. To take extra classes to revise the concepts
2.To check the Coverage of syllabus according to the Internal Examinations.	2. The concerned faculty identified few students are not attending regularly.	2. The related notes and study material is distributed to students for revising.	2. Difficult in understanding the concepts	2. To utilize e-content for explaining more number of problems.

Member	Coordinator	HOD



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Department	ECE	Year/Semester	IV/VII
Course Name	Digital Image and Video Processing	Course Code	1804710

Committee Members:

S.No	Name	Role	Designation	Signature
1.	MdMahaboob Pasha	Coordinator	Asst. Prof	
2.	A ValliBhasha	Member	Asst. Prof	A.V. Bhasha
3.	M Preethi	Member	Asst. Prof	M. preethi.

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To assess the significance of this subject at this level 2. To check the Coverage of syllabus according to the Internal Examinations. 3. To identify and sort out any problems in understanding the subject	1. . Students facing difficulty in understanding mathematical analysis 2. The concerned faculty identified few students are not attending regularly. 3. Students Requested for more practical applications	1. Usage of e content and PPTs for demonstration 2. The related notes and study material is distributed to students for revising. 3. Subject having practical significance in modern applications of AI and ML	1. Lack of awareness in the fundamentals in signal processing 2. Difficult in understanding the concepts .	1. To take extra classes to revise the concepts and signal processing basics 2. To utilize e-content for explaining more number of problems. 3. More quizzes and slip tests must be conducted

Member	Member	Coordinator	HOD

HOD & H.O.D.
Department of E.C.E.
K.S.R.M. College of Engineering
KADAPA - 516 003



Department of Electronics and Communication Engineering
K.S.R.M COLLEGE OF ENGINEERING
AUTONOMOUS
FIRST COURSE REVIEW COMMITTEE REPORT(CRC) 2020-21

Department	ECE	Year/Semester	IV B.Tech/VII
Course Name	CMOS Design	Course Code	1804706

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Smt. S. SharmilaBanu	Coordinator	Asst. Professor	
2.	Smt. K. DivyaLakshmi	Member	Asst. Professor	
3.	K. Lakshmi Prasanna	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To provide rigorous foundation in MOS and CMOS digital circuits 2.To train the students in transistor budgets, clock speeds and the growing challenges of power consumption and productivity .	1. Students are new to the CMOS design based circuits. 2. Some of the students are week in digital circuits and in the basics of VLSI.	1.The students learnt the basics of MOS transistor at the basic level. 2.The easy way of explaining the concepts of designing CMOS circuits based on digital systems.	1.Difficult to analyze the design of circuits using CMOS-from the students point of view.. Difficulty to understand the performance of the circuits.	The practicing sessions are conducted and explained the basics of the designing and the performance of the circuits.

 Smt. K. DivyaLakshmi	 Smt. S. SharmilaBanu	 Dr.G.Hemalatha
Member	Coordinator	Prof. HOD & H.O.D. Department of E.C.E.

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FIRST COURSE REVIEW COMMITTEE REPORT (CRC) 2020-21

Department	ECE	Year/Semester	IV/VII
Course Name	Internet of Things	Course Code	1804701

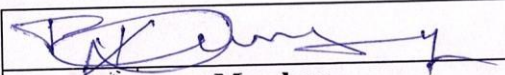
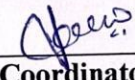
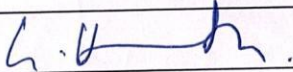
Date: 30/10/2022

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Dr. Syed Zahiruddin	Coordinator	Asso.Prof	
2.	Sri P. Krishna Teja Yadav	Member	Asst.Prof	
3.	Sri. R.V. Suresh	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	

<p>1. To check the Uniform Coverage of syllabus according to the academic calendar.</p> <p>2. To overcome the short comings from students' point of view, if any</p>	<p>1. Student wants to listen to teaching in E-class mode and black boards teaching because they want to understand the topics elaborately.</p> <p>2. The concerned faculty identified few of them are irregular for classes.</p>	<p>1. Usage of E Content, and YOUTUBE videos for explaining each and every topic, by this student is able to understand clearly.</p> <p>2. The related lecture notes handed over before commencement of topics to be covered</p>	<p>1. Difficult to understand the cloud computing concept and Programs related to MSP processor.</p>	<p>1. To design IoT applications and real time Projects using MSP processor and Arduino.</p> <p>2. Explaining more applications related to IoT.</p>
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<p>Member</p>	<p>Coordinator</p>	<p>HOD</p>

Professor & H.O.D.
 Department of E.C.E.
 K.S.R.M. College of Engineering
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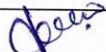
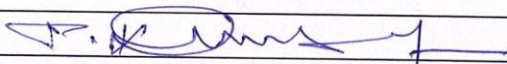
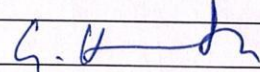
Department	ECE	Year/Semester	IV/VII
Course Name	IoT lab	Course Code	1804713

Committee Members:

Date: 29/10/2021

S.No	Name	Role	Designation	Signature
1.	Sri P. Krishna Teja Yadav	Coordinator	Asst.Prof	
2.	Dr. S. Zahiruddin	Member	Asso.Prof	
3.	Himaja Reddy	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
<p>1. To check the Uniform Coverage of experiments according to the academic calendar.</p> <p>2.To overcome the shortcomings from students' point of view, if any.</p>	<p>1.Student's wanted to listen the programming fundamentals related to MSP processor and its significance.</p> <p>2.The concerned faculty identified a few students are not attending regularly.</p>	<p>1.Usage of YOUTUBE videos for explaining each and Every experiment.</p> <p>2. The related text books and programs are sent before commencement of experiments.</p> <p>3.Lab manuals /handouts are given before/after lab.</p>	<p>1. Difficult to understand the Programs using MSP processor and Tiva Processor.</p>	<p>1. To create a real time Project using MSP processor and Tiva processor for their academic purpose.</p> <p>2. Practicing more Experiments.</p>

		
Member	Coordinator	HOD

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